

## Description

### PECJ N-channel Enhancement Mode Power MOSFET

#### Features

- 60V,35A  
 $R_{DS(ON)} < 18m\Omega$  @  $V_{GS} = 10V$   
 $R_{DS(ON)} < 27m\Omega$  @  $V_{GS} = 4.5V$
- Advanced Trench Technology
- Provide Excellent  $R_{DS(ON)}$  and Low Gate Charge
- Lead free product is acquired

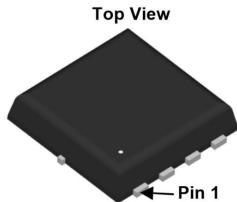
#### Application

- Load Switch
- PWM Application
- Power management

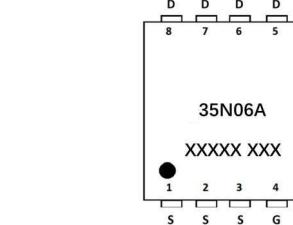
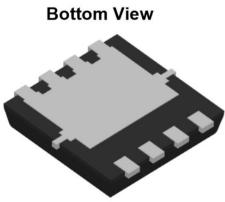


100% UIS TESTED!

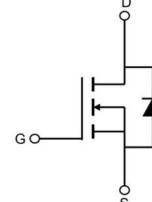
100%  $\Delta V_{ds}$  TESTED!



PDFN3.3X3.3-8L



Marking and pin Assignment



Schematic Diagram

## Package Marking and Ordering Information

Device Marking	Device	OUTLINE	Device Package	Reel Size	Reel (PCS)	Per Carton (PCS)
PECJ35N06A	PECJ35N06A	TAPING	PDFN3.3X3.3-8L	13inch	5000	50000

## Absolute Maximum Ratings ( $T_c=25^\circ C$ unless otherwise specified)

Symbol	Parameter		Max.	Units
$V_{DSS}$	Drain-Source Voltage		60	V
$V_{GSS}$	Gate-Source Voltage		$\pm 20$	V
$I_D$	Continuous Drain Current	$T_c = 25^\circ C$	35	A
		$T_c = 100^\circ C$	22.8	A
$I_{DM}$	Pulsed Drain Current <sup>note1</sup>		140	A
EAS	Single Pulsed Avalanche Energy <sup>note2</sup>		52	mJ
$P_D$	Power Dissipation	$T_c = 25^\circ C$	41	W
$R_{\theta JC}$	Thermal Resistance, Junction to Case		3	$^\circ C/W$
$T_J, T_{STG}$	Operating and Storage Temperature Range		-55 to +150	$^\circ C$

# PECJ35N06A

## Electrical Characteristics ( $T_J=25^\circ\text{C}$ unless otherwise specified)

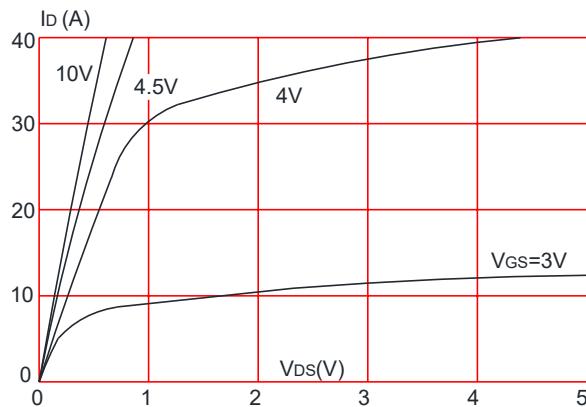
Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
<b>Off Characteristic</b>						
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$ , $I_D=250\mu\text{A}$	60	-	-	V
$I_{\text{DSS}}$	Zero Gate Voltage Drain Current	$V_{DS}=60\text{V}$ , $V_{GS}=0\text{V}$ ,	-	-	1.0	$\mu\text{A}$
$I_{GSS}$	Gate to Body Leakage Current	$V_{DS}=0\text{V}$ , $V_{GS}=\pm 20\text{V}$	-	-	$\pm 100$	nA
<b>On Characteristics</b>						
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS}=V_{GS}$ , $I_D=250\mu\text{A}$	1.0	1.6	2.5	V
$R_{DS(\text{on})}$ note3	Static Drain-Source on-Resistance	$V_{GS}=10\text{V}$ , $I_D=30\text{A}$	-	12	18	$\text{m}\Omega$
		$V_{GS}=4.5\text{V}$ , $I_D=20\text{A}$	-	15	27	
<b>Dynamic Characteristics</b>						
$C_{iss}$	Input Capacitance	$V_{DS}=25\text{V}$ , $V_{GS}=0\text{V}$ , $f=1.0\text{MHz}$	-	2900	-	pF
$C_{oss}$	Output Capacitance		-	140	-	pF
$C_{rss}$	Reverse Transfer Capacitance		-	124	-	pF
$Q_g$	Total Gate Charge	$V_{DS}=30\text{V}$ , $I_D=30\text{A}$ , $V_{GS}=10\text{V}$	-	50	-	nC
$Q_{gs}$	Gate-Source Charge		-	6	-	nC
$Q_{gd}$	Gate-Drain("Miller") Charge		-	15	-	nC
<b>Switching Characteristics</b>						
$t_{d(on)}$	Turn-on Delay Time	$V_{DS}=25\text{V}$ , $I_D=30\text{A}$ , $R_G=1.8\Omega$ , $V_{GS}=10\text{V}$	-	7.4	-	ns
$t_r$	Turn-on Rise Time		-	5.1	-	ns
$t_{d(off)}$	Turn-off Delay Time		-	28.2	-	ns
$t_f$	Turn-off Fall Time		-	5.5	-	ns
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
$I_s$	Maximum Continuous Drain to Source Diode Forward Current	-	-	35	-	A
$I_{sM}$	Maximum Pulsed Drain to Source Diode Forward Current	-	-	140	-	A
$V_{SD}$	Drain to Source Diode Forward Voltage	$V_{GS}=0\text{V}$ , $I_s=30\text{A}$	-	-	1.2	V
$trr$	Body Diode Reverse Recovery Time	$I_F=30\text{A}$ , $dI/dt=100\text{A}/\mu\text{s}$	-	28	-	ns
$Qrr$	Body Diode Reverse Recovery Charge		-	40	-	nC

Notes: 1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

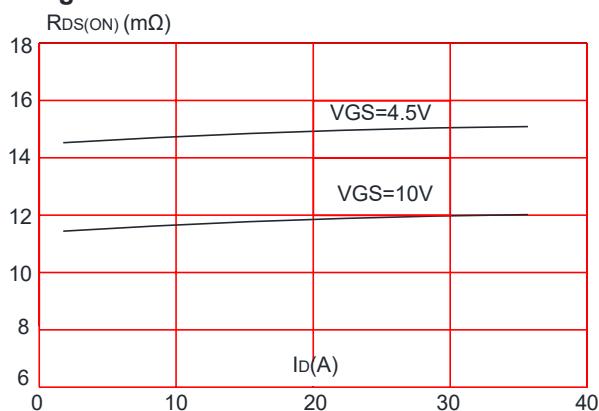
2. EAS condition :  $T_J=25^\circ\text{C}$ ,  $V_{DD}=30\text{V}$ ,  $V_C=10\text{V}$ ,  $L=0.5\text{mH}$ ,  $R_g=25\Omega$ ,  $I_{AS}=14.4\text{A}$

3. Pulse Test: Pulse Width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 0.5\%$

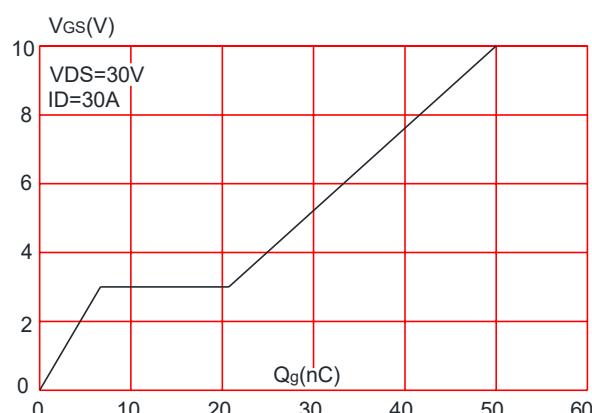
**Figure1:** Output Characteristics



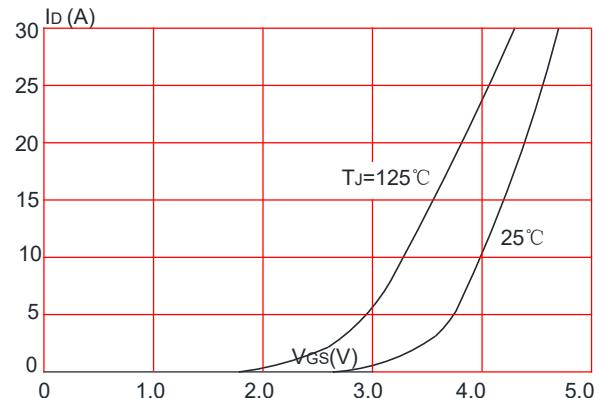
**Figure 3:** On-resistance vs. Drain Current



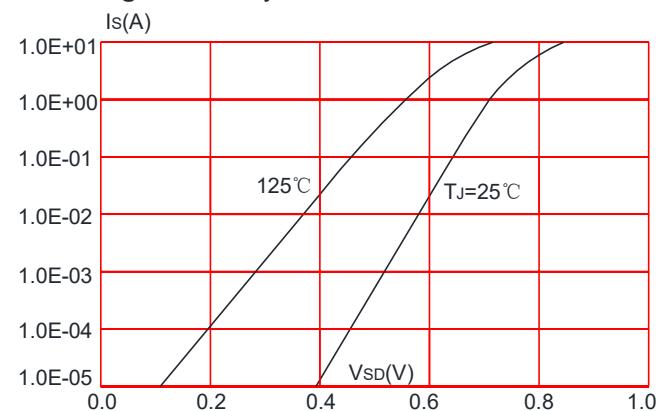
**Figure 5:** Gate Charge Characteristics



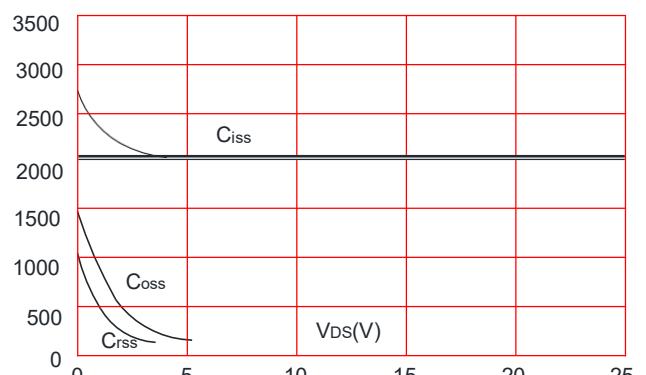
**Figure 2:** Typical Transfer Characteristics



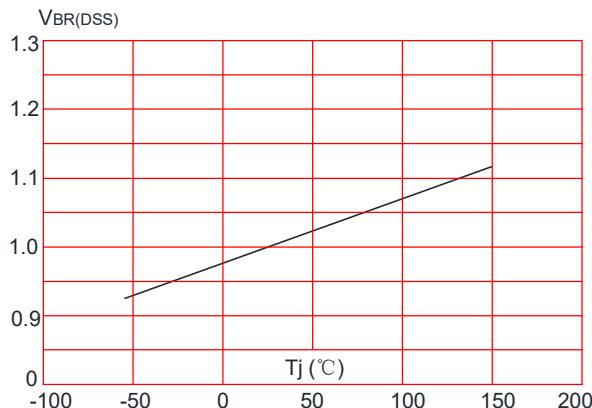
**Figure 4:** Body Diode Characteristics



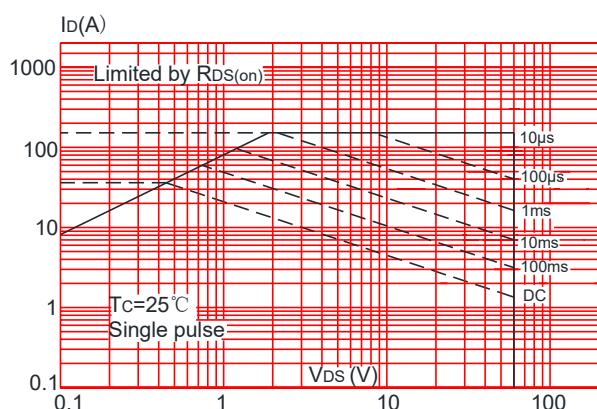
**Figure 6:** Capacitance Characteristics



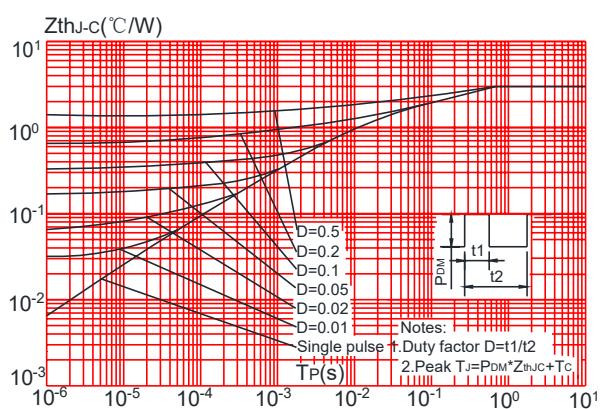
**Figure 7:** Normalized Breakdown Voltage vs. Junction Temperature



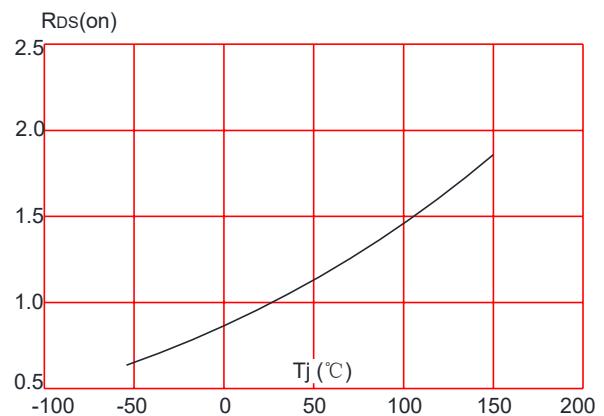
**Figure 9:** Maximum Safe Operating Area



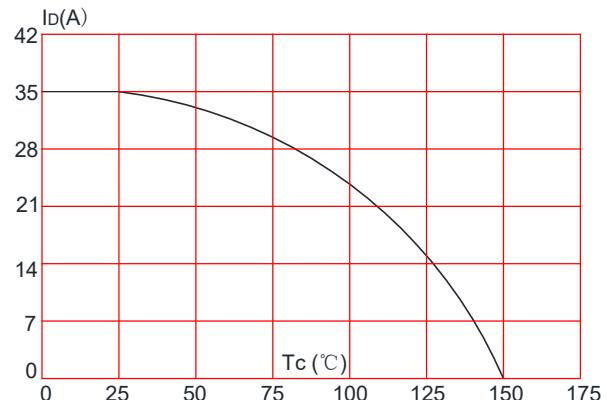
**Figure 11:** Maximum Effective Transient Thermal Impedance, Junction-to-Case



**Figure 8:** Normalized on Resistance vs. Junction Temperature



**Figure 10:** Maximum Continuous Drain Current vs. Case Temperature



## Test Circuit

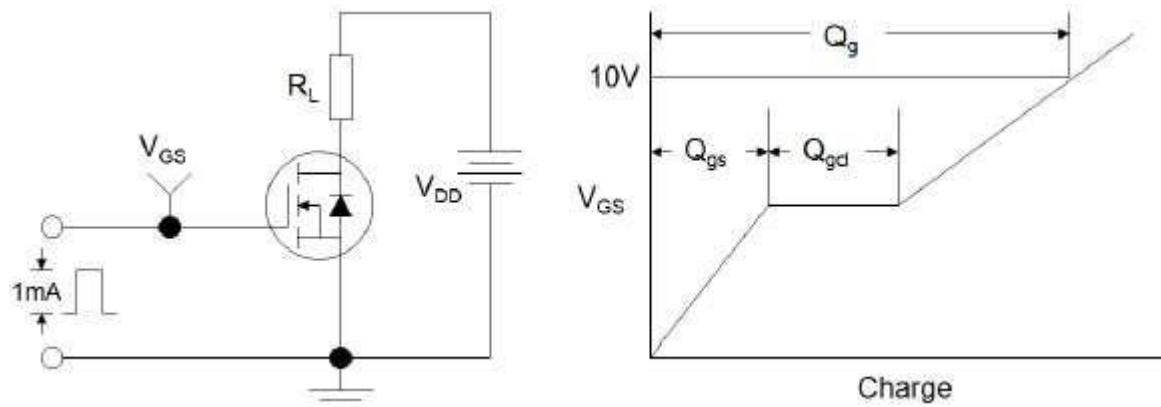


Figure 1: Gate Charge Test Circuit & Waveform

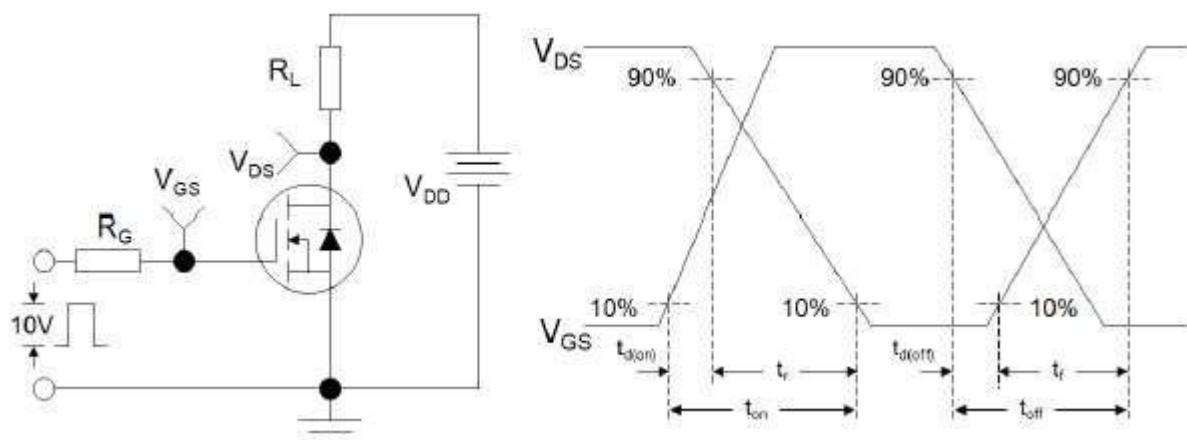


Figure 2: Resistive Switching Test Circuit & Waveforms

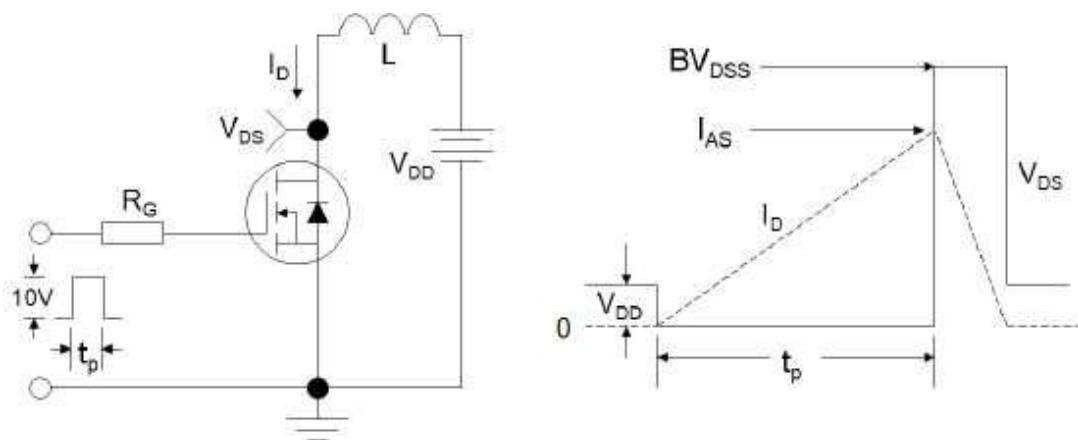
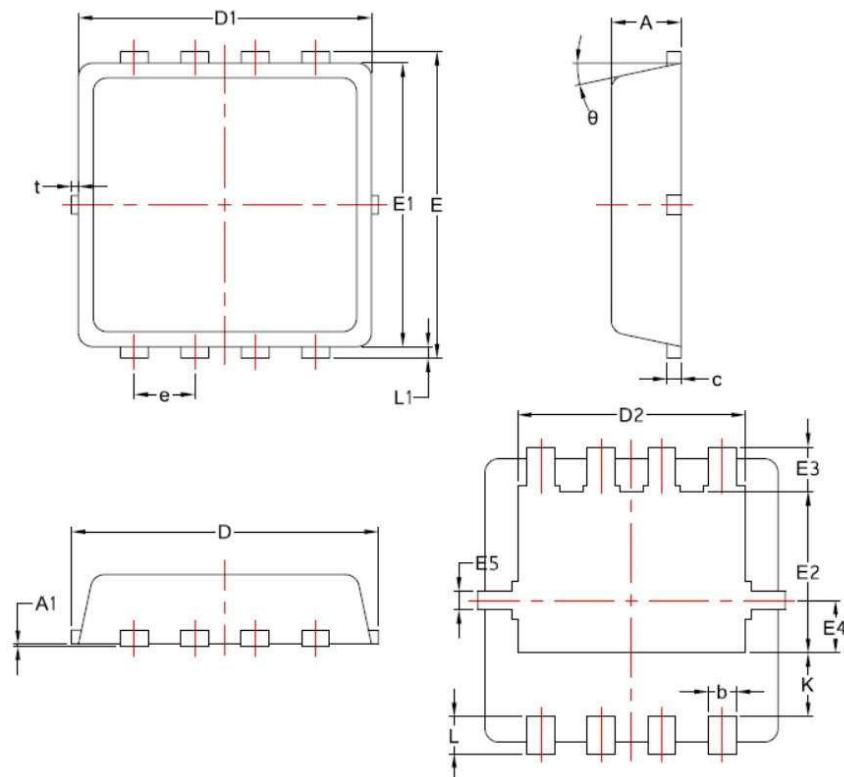


Figure 3: Unclamped Inductive Switching Test Circuit & Waveforms

## Package Mechanical Data- PDFN3.3X3.3-8L



SYMBOL	COMMON		
	MM		
	MIN	NOM	MAX
A	0.70	0.75	0.85
A1	/	/	0.05
b	0.20	0.30	0.40
c	0.10	0.152	0.25
D	3.15	3.30	3.45
D1	3.00	3.15	3.25
D2	2.29	2.45	2.65
E	3.15	3.30	3.45
E1	2.90	3.05	3.20
E2	1.54	1.74	1.94
E3	0.28	0.48	0.65
E4	0.37	0.57	0.77
E5	0.10	0.20	0.30
e	0.60	0.65	0.70
K	0.59	0.69	0.89
L	0.30	0.40	0.50
L1	0.06	0.125	0.20
t	0	0.075	0.13
θ	10°	12°	14°